

ABSTRACT

A dielectric layer is formed on a surface of a porous portion formed at least on a surface of a valve metal sheet. A solid electrolyte layer is formed on the dielectric layer, and a collector layer is formed on the solid electrolyte layer. A first insulating portion is formed on an outer periphery of the dielectric layer, and a solid electrolyte layer is formed on a portion of the dielectric layer corresponding to an opening of the first insulating portion. A portion of the solid electrolyte layer is formed on the first insulating portion. A second insulating portion is formed on the first insulating portion and on the outer periphery of the solid electrolyte layer. A collector layer is formed on a surface of the solid electrolyte layer exposed through an opening of the second insulating portion, thus providing a solid electrolytic capacitor. The solid electrolytic capacitor has less current leakage and a high withstand voltage.